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with modest effort. The ROCKET compiler, a retargetable C compiler for LongInstructionWord (LIW) and
www.lance.colostate.edu/~beaty/Papers/total.ps

Simple Register Spilling In a Retargetable Compiler - Fraser, Hanson (1995) (Correct) (8 citations)
the management of register spills in a retargetable C compiler. Spills are rare, which means that
(month 1995) Simple Register Spilling In A Retargetable Compiler Christopher W. Fraser At&I Bell
ftp.cs.princeton.edu/pub/lcc/contrib/spills.ps.gz

A Machine-Independent Debugger - David Hanson And (1995) (Correct) (5 citations)
www.cs.princeton.edu/~drh/pubs/cdb.pdf

FRIGG: A New Approach to Combining Register Assignment and.. - Brasier (1994) (Correct) (2 citations)
this algorithm has been implemented in a retargetable C Compiler and, through testing, it has been found
cs.mtu.edu/pub/sweany/brasier.thesis.ps

Early Experience with ASDL in lcc - Hanson (1999) (Correct) (2 citations)
www.research.microsoft.com/~drh/pubs/asdl.pdf

Bottom-up Tree Rewriting Tool MBURG - John Gough (1996) (Correct) (2 citations)
for code selection in the widely available retargetable C compiler lcc[2]The gardens point compilers
www.dstc.qut.edu.au/~gough/sigplan.ps

A System-level Energy Minimization Approach Using Datapath... - Cao, Yasuura (2001) (Correct) (1 citation)
kasuga.csce.kyushu-u.ac.jp/~cao/paper/isipcd_01.ps

Recovery of Jump Table Case Statements from Binary Code - Cifuentes, Van Emmerik (1999) (Correct) (1 citation)
Indirect jump through a branch table for a retargetable C compiler is given in [14] in the following
www.csee.uq.edu.au/~cristina/tr444.ps

A Programming Language for Processor Based Embedded Systems - Akihiko Inoue (1998) (Correct) (1 citation)
are described. The structure of the ValenC retargetable compiler which we have developed and how to
in detail. The structure of the ValenC retargetable compiler and how the correctness of ValenC
along with the structure of the ValenC retargetable compiler are described. 3. ValenC and The
kasuga.csce.kyushu-u.ac.jp/~inoe/papers/apchd198.ps.gz

Efficient code generation for ASIPs with different word sizes - Andrea Cilio (1997) (Correct) (1 citation)
methodology, developed on the basis of a retargetable C compiler (the SUIF compiler) requires the
cardit.et.tudelft.nl/MOVE/papers/Cilio97a.ps

Resume of Grigori R. Humphreys - Humphreys (2002) (Correct)
program, in the style of Hanson's A Retargetable C Compiler and Knuth's T E X: The Program. Drafts
graphics.stanford.edu/~humper/cv.pdf

The lcc 4.x Code-Generation Interface - Christopher Fraser And (Correct)
compiler for Standard C described in A Retargetable C Compiler: Design and Implementation. This report
www.research.microsoft.com/~drh/pubs/interface4.pdf

A Retargetable C* Compiler and Run-time Library for.. - Anthony Lapadula And (Correct)
A Retargetable C* Compiler and Run-time Library for Mesh-Connected
students.cs.byu.edu/~clement/cs584/TR92-15.ps

Building Real Computer Systems - Uht, Lo, Sun, Daly, Kowalski (2000) (Correct)
ele.uri.edu/pub/uht/teeemicro2000.pdf

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www.ece.gatech.edu/users/hamblen/ALTERA/wcae98.PDF

Use of CAD Tools in the Integrated Computer Engineering Design... - Uht (1997) (Correct)
ele.uri.edu/pub/uht/rmg97.PS.gz

Compiler Predication - Postiff (Correct)
[2] Christopher Fraser and David Hanson, A Retargetable C Compiler: Design and Implementation. Benjamin/
www-personal.umich.edu/~postiff/papers/S83report.fm.ps

The Integrated Computer Engineering Design (ICED) Curriculum - Uht (Correct)
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Evaluating Runtime-Compiled Value-Specific Optimizations - Keppel, Eggers, Henry (1993) (Correct) (24 citations)

ftp.cs.washington.edu/tr/1993/11/UW-CSE-93-11-02.PS.Z

Retargetable Instruction Scheduling for Pipelined Processors - Bradlee (1991) (Correct) (13 citations)

[2] D. Bradlee. Retargetable Instruction Scheduling for Pipelined
ftp.cs.washington.edu/tr/1991/08/UW-CSE-91-08-07.PS.Z

Software Synthesis for Real-Time Information.. - Cornaro, Thoen.. (1995) (Correct) (11 citations)

can be easily achieved by means of a retargetable code generator. This is especially inter esting when
www.imec.be/vsdm/projects/sw_synthesis/././ftp/pub/sw_synthesis/mc-dagstuhl95.ps

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this problem. Mimola 12] contains a retargetable code generator for a class of non-DSP architectures.
ftp.imec.be/pub/vsdm/reports/retargetable_code_generation/gg-jvsp94.ps.gz

Application of Constraint Logic Programming for VLSI CAD... - Beckmann, Bieker, Markhof (1994) (Correct) (4 citations)

description language. Afterwards, a retargetable code generator is described. With this tool it is
ts12-www.informatik.uni-dortmund.de/publications/papers/1994-ccl.ps.gz

Beyond Tool-Specific Machine Descriptions - Fauth (1995) (Correct) (3 citations)

nML is presented along with the retargetable code generator Cbc and the instruction set simulation
www.techfak.uni-bielefeld.de/techfak/ags/i/forschung/publikationen/codegen.ps.gz

Instruction Selection, Resource Allocation, and Scheduling in ... - Hanono, Devadas (1998) (Correct) (3 citations)

Allocation, and Scheduling in the AVIV Retargetable Code Generator Silvina Hanono Srinivas Devadas
glon.lcs.mit.edu/~devadas/pubs/aviv.ps

Machine-Independent Support for Garbage Collection, Debugging, ... - Jones, Ramsey (1998) (Correct) (2 citations)

is hard work. One might try to reuse a retargetable code generator from another compiler, but code
ftp.cs.virginia.edu/pub/techreports/CS-98-19.ps.Z

Description and Simulation of Microprocessor Instruction... - Teich, Kutter, Weper (2000) (Correct) (1 citation)

ftp-date.uni-paderborn.de/pub/people/weper/paper/TKW.ps.gz

A Retargetable Code Generator for SUIF - Markwardt, Schulz (1996) (Correct) (1 citation)

A Retargetable Code Generator for SUIF Gert Markwardt
suif.stanford.edu/suifconf/suifconf1/papers/paper1.ps

Aviv: A Retargetable Code Generator for Embedded Processors - Hanono (1999) (Correct) (1 citation)

Aviv: A Retargetable Code Generator for Embedded Processors by Silvina
caa.lcs.mit.edu/~silvina/thesis/phd_thesis.ps.gz

Instructions Scheduling for Highly Super-scalar Processors - Appelbe, Das, Harmon (1997) (Correct) (1 citation)

simultaneously. Work with the Marion retargetable code generator [3] has shown that for Inorder
ftp.cc.gatech.edu/pub/coc/tech_reports/1997/GIT-CC-97-34.ps.Z

Design and Implementation of the GNU INSEL-Compiler gic - Pizka (1997) (Correct) (1 citation)

. 10 2.1.2 gcc as a Retargetable CodeGenerator .
www.spies.informatik.tu-muenchen.de/personen/pizka/gic.ps.gz

ASIP Design Methodologies: Survey and Issues - Jain, Balakrishnan, Kumar (2001) (Correct)

www.cse.iitd.ernet.in/esproject/docs/projects/manoj/asip_survey.ps

Heuristic Tradeoffs Between Latency And Energy.. - Anand, Acome, De Vedana (2000) (Correct)

www.sigda.org/Archives/ProceedingArchives/Codes/Codes2000/papers/2000/codes00/htmlfiles/SUN_SGI/././pdffiles/06_2.pdf

Publications of Leon Moonen - Moonen (Correct)

www.cwi.nl/~leon/papers/publications_leon_moonen.ps.gz

A Virtual Assembler for an Abstract Machine - Moonen (1994) (Correct)

www.cwi.nl/~leon/papers/thesis/thesis.ps.gz

Design Space Characterization for... - Fischer, Teich.. (Correct)

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